



FIG. 7



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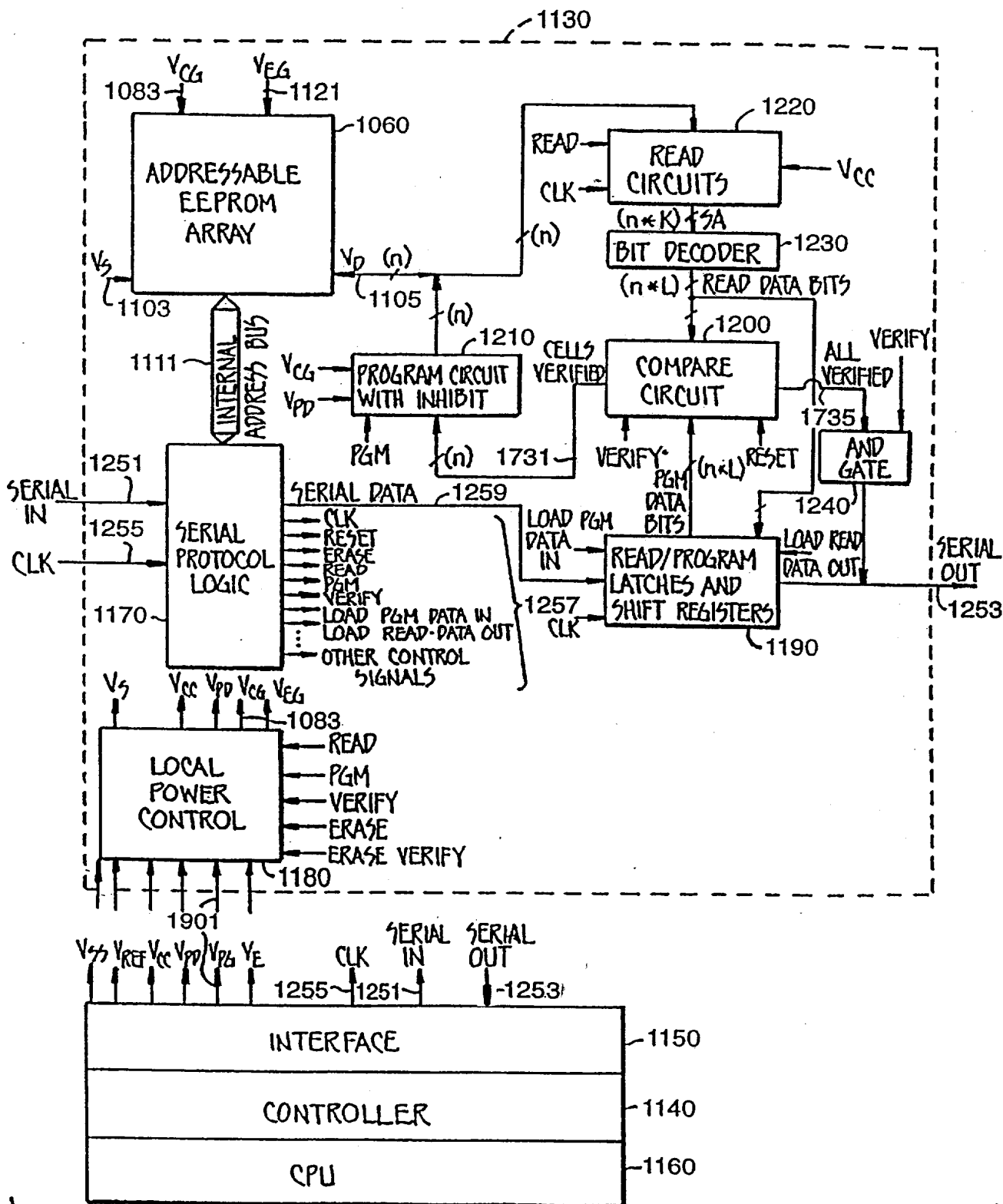


FIG. 13

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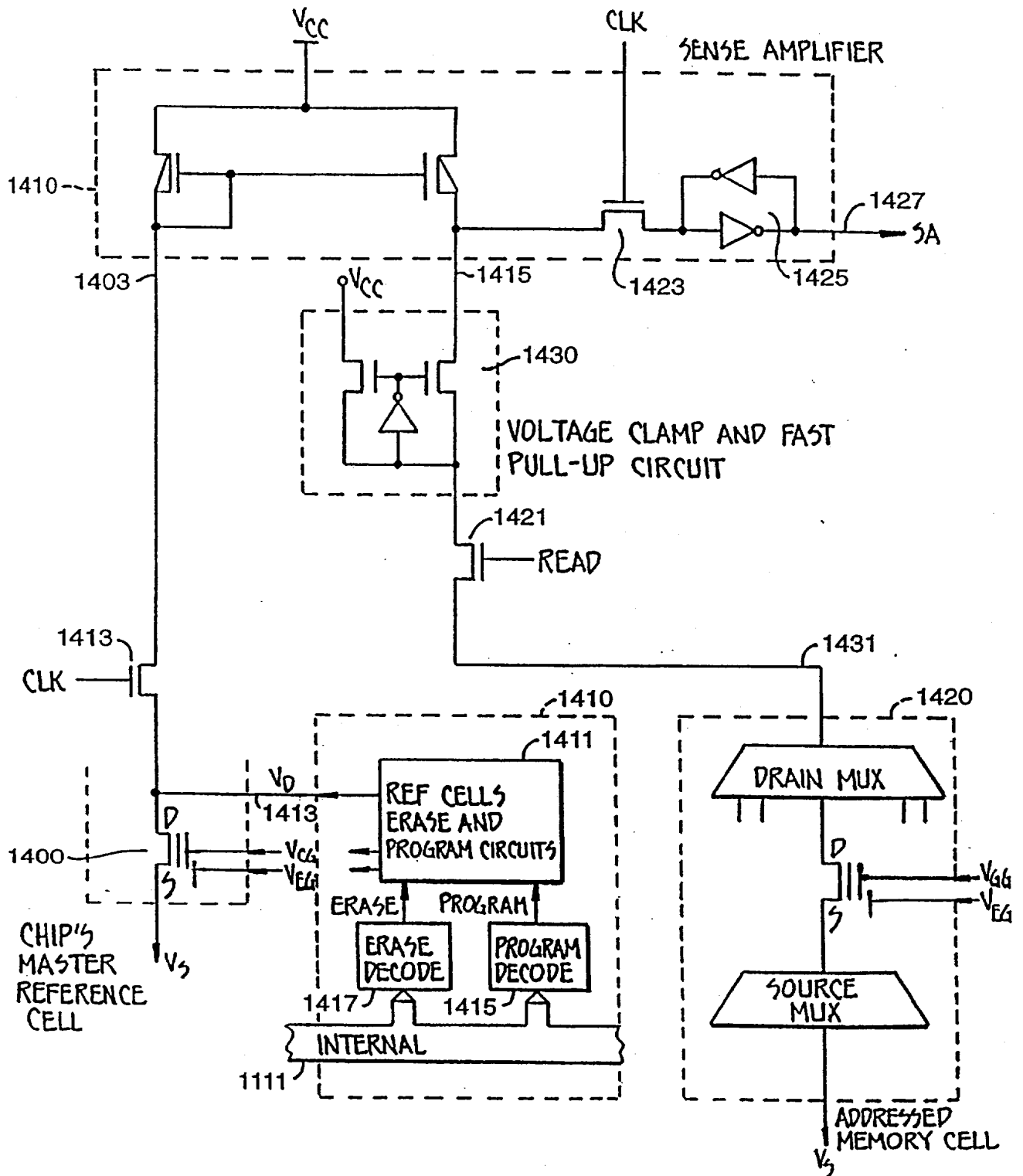


FIG. 17A

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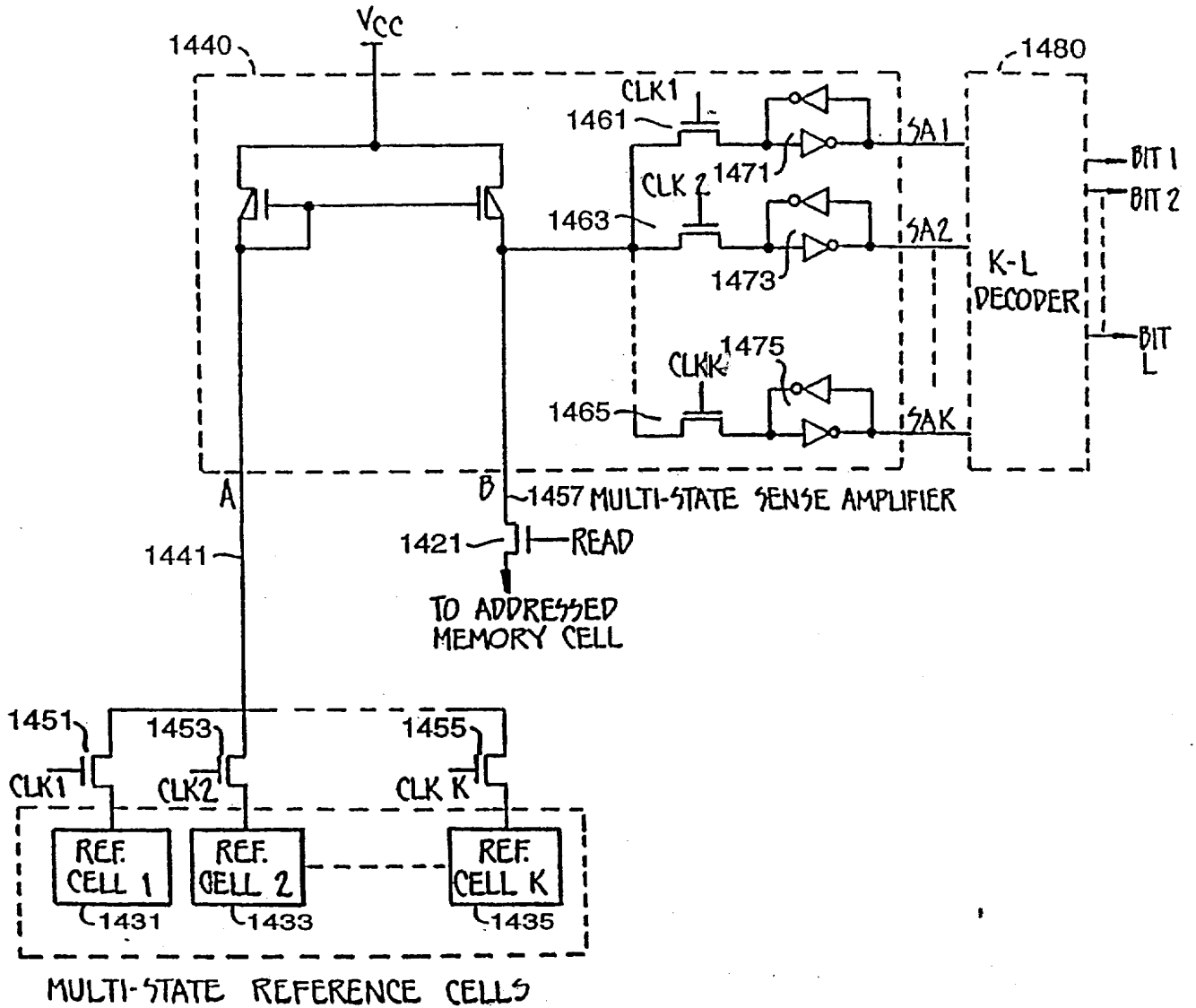


FIG. 17B

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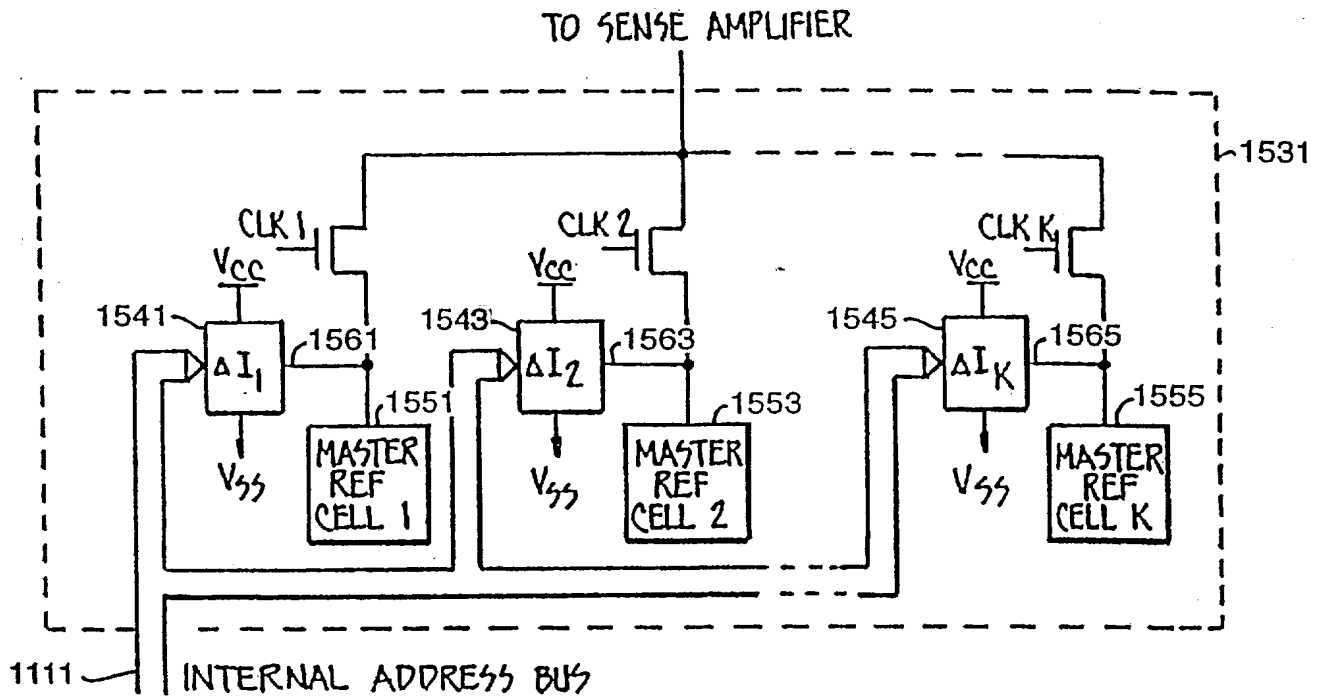


FIG. 21B

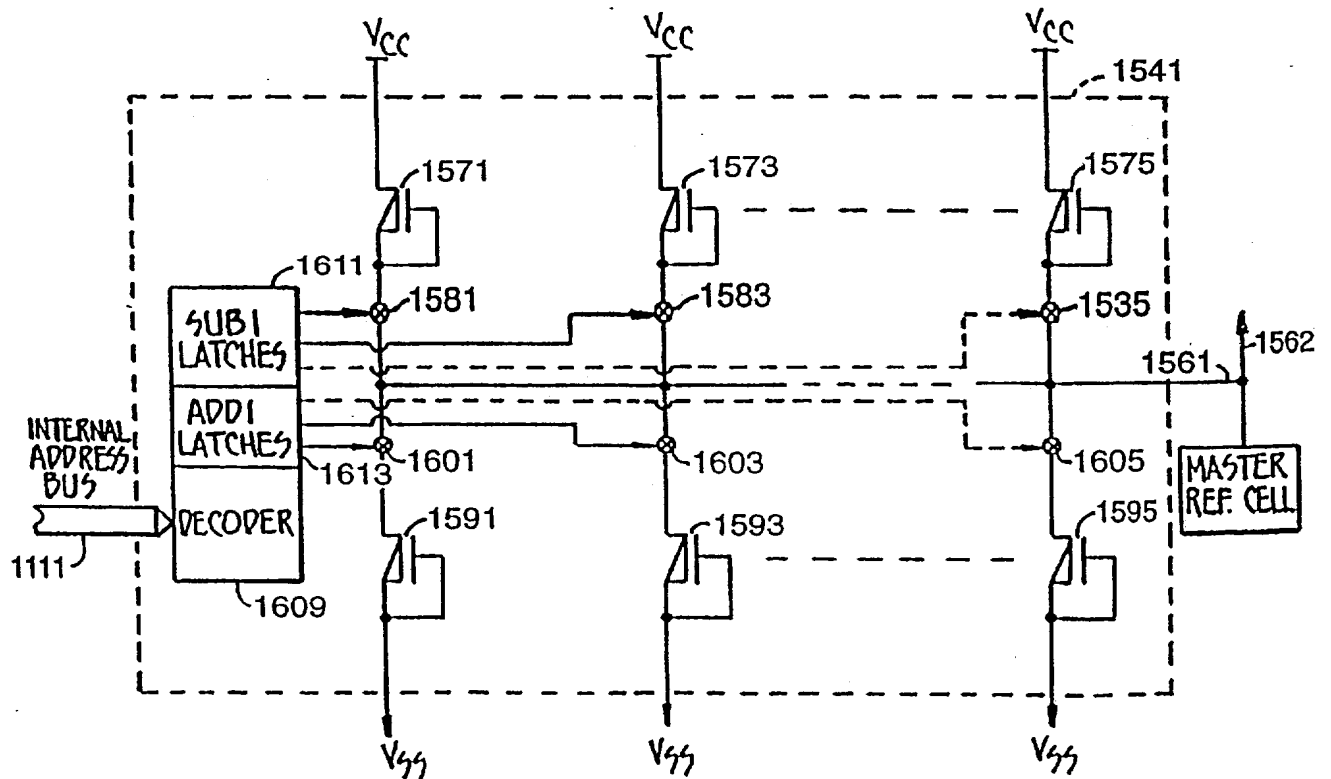
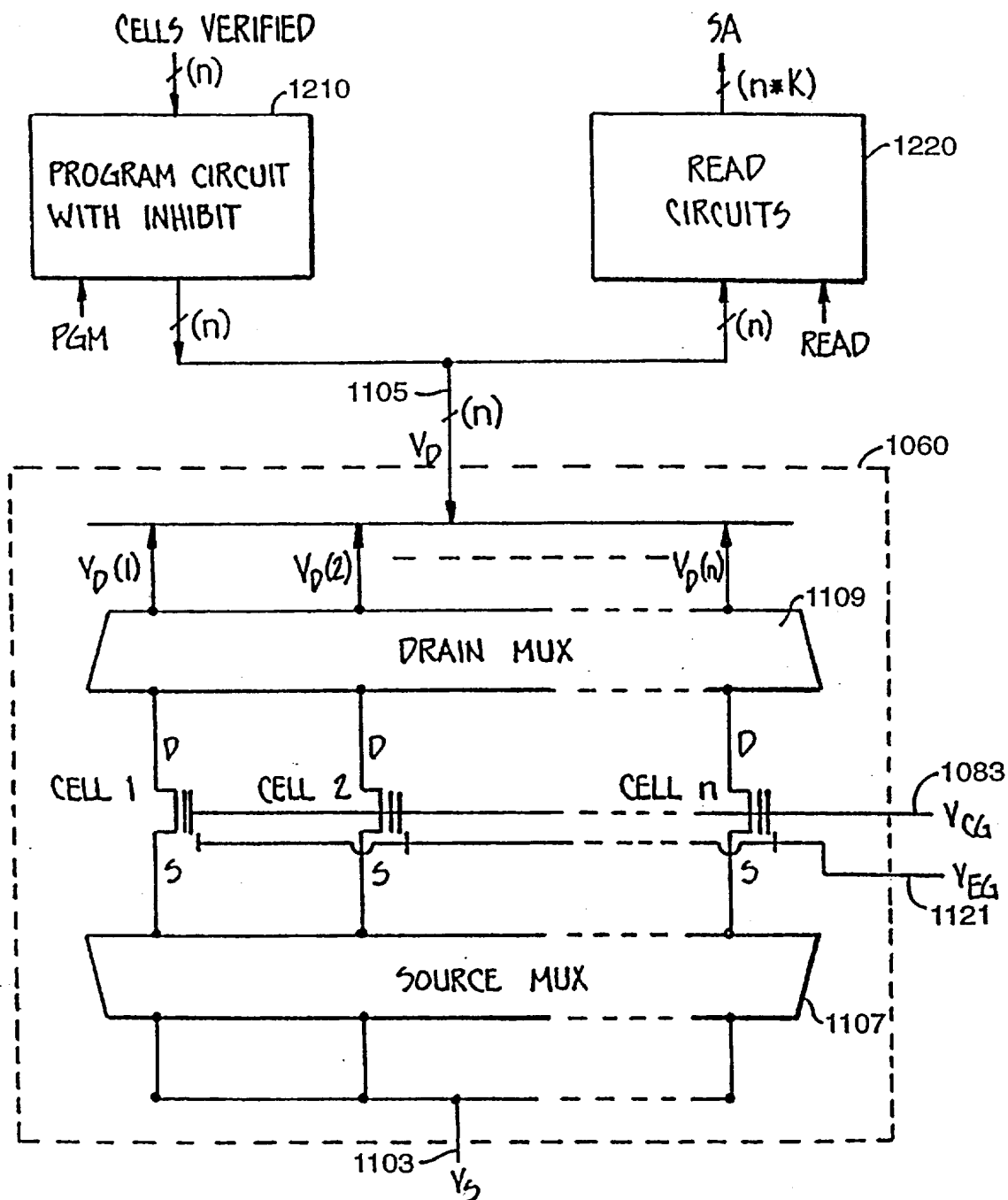


FIG. 21C

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READ/PROGRAM DATA PATHS FOR n CELLS IN PARALLEL

FIG. 22